

## REMARKS

The objections in paragraphs 3-7 have been cured by appropriate amendments. Reconsideration of the requirement of a summary is respectfully requested on the grounds that the operative rule, 37 C.F.R. 1.73, only indicates that "... such a summary should, when set forth, be commensurate...." This language clearly indicates that the provision of a summary is optional.

Claim 1 was rejected over a prior patent to some of the inventors of the present application.

However, that prior patent does not teach "a set of instructions to ensure that the second thread copies data based, in part, on a first descriptor associated with the first thread" set forth in claim 1. Similar language is also contained in claims 9, 18, and 22. Therefore, reconsideration of the rejection of those claims and the claims dependent thereon is respectfully requested.

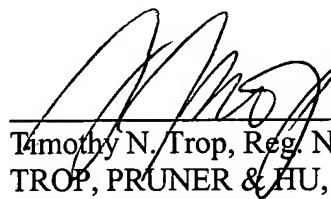
The subject material is explained in the specification at page 10, starting at line 20. There, it is indicated that the parallel threads may execute instructions in block 513, shown in Figure 5B. These instructions cause parallel threads to set up descriptors, such as the descriptor 801 in Figure 8, and may specify upper and lower bounds of a private array to be copied. In some embodiments, the parallel threads, after having established their descriptors in block 513, then call a subroutine shown in Figure 6. The call instruction to call the subroutine, at block 515, passes a number of variables to the subroutine that may execute the copy routines necessary to support the copyprivate clause by, in one embodiment, copying data from the single thread's memory area to other parallel thread's memory areas. The descriptors may define where to copy data from and where to copy data to in the memory areas shown in Figure 8.

The subroutine, shown in Figure 6, determines whether a single thread or multiple threads are involved. If the thread is a single thread, it executes the instructions in block 605 by copying the address of the single thread's descriptor to an active buffer. If multiple parallel threads are involved, the parallel threads may use the single thread's descriptor to copy the single thread's data into the other parallel thread's private memory area, as described by the other parallel thread's descriptors. See the specification at page 12, line 27, through page 13, line 5.

No such operation is anywhere suggested in the prior Poulsen patent. In other words, there is no teaching that the instructions to ensure that the second thread copies data based, in part, on a first description associated with the first thread is nowhere suggested. Therefore, reconsideration of the rejection is respectfully requested.

Respectfully submitted,

Date: March 7, 2005



---

Timothy N. Trop, Reg. No. 28,994  
TROP, PRUNER & HU, P.C.  
8554 Katy Freeway, Ste. 100  
Houston, TX 77024  
713/468-8880 [Phone]  
713/468-8883 [Fax]

Attorneys for Intel Corporation